

**AMENDMENTS TO THE CLAIMS**

1. (Currently Amended) A linking method under a mother and child block architecture for building a check area and a ~~logic~~-logical page of a child block in a flash memory, wherein when a host writes data into a ~~logic~~-logical block of said flash memory, the ~~writing~~-linking method comprising:

defining a block corresponding to said ~~logic~~-logical block as a mother block;

~~locating~~-locating a new block from a backup block and defining said new block as a child block, wherein said mother block and said child block ~~are directed at said logic block~~ have the same logical address;

recording the data into a page of said child block, while retaining original data in said mother block;

using a redundant page, which stores metadata in said child block for creating a check area;

recording said redundant page of said child block ~~belonging to which corresponds to a~~ page in said mother block; and

using said check area consisting of a ~~logic~~-logical page for identifying whether the data to be retrieved is ~~from~~-stored in said mother block or child block ~~in the subsequent time when a~~ subsequent read is performed.

2. (Currently Amended) The linking method under a mother and a child block architecture for building a check area and a ~~logic~~-logical page of a child block according to claim 1, wherein when a host is ready for reading said page in said ~~logic~~-logical page, said child block

corresponding to said mother block in said ~~logie~~-logical page is read and ~~the remaining~~  
~~unmarked-other~~ pages in said child block are read all from said pages of said mother block.

3. (Currently Amended) The linking method under a mother and a child block architecture for building a check area and a ~~logie~~-logical page of a child block according to claim 1, wherein said ~~redundant page check area~~ in said page of child block is defined as three bytes, wherein a first and a second byte ~~represent as~~ indicate pages of said child block and a third byte ~~represents~~ indicates a page of said mother block.

4. (Currently Amended) The linking method under a mother and a child block architecture for building a check area and a ~~logie~~-logical page of a child block according to claim 1, wherein when said host repeats writing data into said ~~logie area~~ logical page of the child block, the data ~~being continuously written into said page to a full page~~ of said child block and when said logical page of child block is full, a new block is located for moving a valid block of said mother block and said child block into therein and then said mother block and child block are erased.

5. (Currently Amended) A linking method under a mother and a child block architecture for building a check area and a ~~logie~~-logical page of a child block in a flash memory, wherein when a host writes data into a ~~logie~~-logical block of said flash memory, the writing method comprising:

(a) defining a ~~an~~ actual page corresponding to a ~~logie~~-logical page as a mother block;

- (b) locating a new block and defining it as a child block;
- (c) writing data into said logical page of said child block while retaining original data in  
said mother block; and
- (d) marking a ~~redundant~~ check area of said redundant page in said child block belonging  
to a page of said mother block.